



(12) **EUROPEAN PATENT APPLICATION**
published in accordance with Art. 158(3) EPC

(43) Date of publication:
02.01.2003 Bulletin 2003/01

(51) Int Cl.7: H04L 27/38

(21) Application number: 01930214.0

(86) International application number:
PCT/JP01/04182

(22) Date of filing: 18.05.2001

(87) International publication number:
WO 02/065723 (22.08.2002 Gazette 2002/34)

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR

- SUZUKI, Kuniyuki, c/o MITSUBISHI DENKI K.K.
Tokyo 100-8310 (JP)
- DUAN, Jinsong, c/o MITSUBISHI DENKI K.K.
Tokyo 100-8310 (JP)
- FUJIHARA, Nobuo, c/o MITSUBISHI DENKI K.K.
Tokyo 100-8310 (JP)
- YAMAGUCHI, Nobuyasu,
c/o MITSUBISHI DENKI K.K.
Tokyo 100-8310 (JP)
- YAMAZAKI, Takuya, c/o MITSUBISHI DENKI K.K.
Tokyo 100-8310 (JP)

(30) Priority: 13.02.2001 WOPCT/JP01/00983

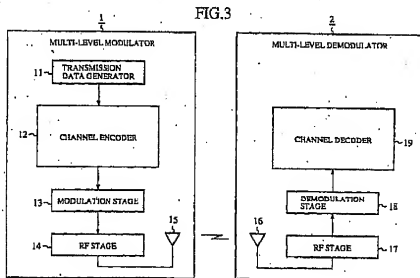
(71) Applicant: MITSUBISHI DENKI KABUSHIKI
KAISHA
Chiyoda-ku Tokyo 100-8310 (JP)

(72) Inventors:
• TAKANO, M., c/o MITSUBISHI DENKI K.K.
Tokyo 100-8310 (JP)
• ABE, Minoru, c/o MITSUBISHI DENKI K.K.
Tokyo 100-8310 (JP)

(74) Representative: Popp, Eugen, Dr. et al
MEISSNER, BOLTE & PARTNER
Postfach 86 05 24
81633 München (DE)

(54) MULTILEVEL MODULATING METHOD, MULTILEVEL DEMODULATING METHOD, AND
MULTILEVEL MODULATING/DEMULATING METHOD

(57) A bit stream of data is mapped in such a manner that a specified bit of at least one symbol becomes a dummy
bit.



Description

TECHNICAL FIELD

[0001] The present invention relates to a multi-level modulation method, multi-level demodulation method and multi-level modulation and demodulation method for modulating or demodulating data consisting of a plurality of symbols.

BACKGROUND ART

[0002] As shown in Fig. 1, a conventional multi-level modulation method transmits data consisting of a plurality of symbols by passing the data through multi-level modulation (Fig. 1 shows an example in which each symbol undergoes 4-bit 16 QAM). When the number of bits of the data is less than that of a radio frame as shown in Fig. 2, however, the data cannot undergo the multi-level modulation. Thus, the data is provided with additional dummy bits to be interleaved.

[0003] Although this enables the data to pass through the multi-level modulation, since the dummy bits are DTX bits other than "0" or "1", and the insertion position of each dummy bit is determined randomly with respect to the symbols, the transmission power of each symbol after interleaving (symbol including a dummy bit) can either increase or decrease.

[0004] With the foregoing configuration, the conventional multi-level modulation method has the following problem: The transmission power of the symbol constituting the data is not always small, and the increase in the transmission power of the symbol causes a problem of increasing the interference to other signals.

[0005] The present invention is implemented to solve the foregoing problem. Therefore, an object of the present invention is to provide a multi-level modulation method and multi-level modulation and demodulation method capable of reducing the transmission power of the symbol and the interference to other signals.

[0006] Another object of the present invention is to provide a multi-level demodulation method and multi-level modulation and demodulation method capable of receiving data with small interference to other signals and of demodulating the data.

DISCLOSURE OF THE INVENTION

[0007] According to a first aspect of the present invention, there is provided a multi-level modulation method of mapping a bit stream of data in such a manner that a specified bit of at least one symbol becomes a dummy bit.

[0008] Thus, it offers an advantage of being able to reduce the transmission power of the symbol and the interference to other signals.

[0009] According to a second aspect of the present invention, there is provided a multi-level demodulation

method of performing multi-level demodulation of data considering that a specified bit of at least one symbol is a dummy bit.

[0010] Thus, it offers an advantage of being able to receive data with small interference to other signals, and to demodulate the data.

[0011] According to a third aspect of the present invention, there is provided a multi-level modulation and demodulation method including the steps of: mapping, when a number of bits of data consisting of a plurality of symbols is less than a number of bits of a radio frame, a bit stream of the data such that a specified bit of at least one symbol becomes a dummy bit; transmitting the data after mapping by passing the data through multi-level modulation; and performing, when receiving the data, the multi-level demodulation of the data considering that at least one specified bit of the symbol is a dummy bit.

[0012] Thus, it offers an advantage of being able to reduce the transmission power of the symbol and the interference to other signals.

[0013] Here, when the symbol undergoes 4-bit 16 QAM in the multi-level modulation and demodulation method in accordance with the present invention, a dummy bit of "0" may be assigned to the lowest two bits of the symbol.

[0014] Thus, it offers an advantage of being able to reduce the transmission power of the symbol.

[0015] When the symbol undergoes 4-bit 16 QAM in the multi-level modulation and demodulation method in accordance with the present invention, transmission power may be turned off.

[0016] Thus, it offers an advantage of being able to reduce the transmission power of the symbol.

[0017] When the symbol undergoes 6-bit 64 QAM in the multi-level modulation and demodulation method in accordance with the present invention, a dummy bit of "0" may be assigned to intermediate two bits of the symbol.

[0018] Thus, it offers an advantage of being able to reduce the transmission power of the symbol.

[0019] When the symbol undergoes 6-bit 64 QAM in the multi-level modulation and demodulation method in accordance with the present invention, a dummy bit of "0" may be assigned to intermediate two bits of the symbol and a dummy bit of "1" may be assigned to the lowest two bits of the symbol.

[0020] Thus, it offers an advantage of being able to reduce the transmission power of the symbol.

[0021] When the symbol undergoes 6-bit 64 QAM in the multi-level modulation and demodulation method in accordance with the present invention, a dummy bit of "0" may be assigned to all the bits of the symbol.

[0022] Thus, it offers an advantage of being able to reduce the transmission power of the symbol.

[0023] When the dummy bits are added to the data in the multi-level modulation and demodulation method in accordance with the present invention, the dummy bits may be distributed to a plurality of symbols.

[0024] Thus, it offers an advantage of being able to prevent error because of the effect of fading.

[0025] In the multi-level modulation and demodulation method in accordance with the present invention, two dummy bits may be assigned to each of the symbols, and when the dummy bits are left even after assigning the two dummy bits to each of all the symbols, another two bits may be assigned to some of the symbols.

[0026] Thus, it offers an advantage of being able to prevent the error because of the effect of fading, and to reduce the transmission power of the data as a whole.

[0027] When carrying out multi-code transmission of the data in the multi-level modulation and demodulation method in accordance with the present invention, the dummy bits may be mapped in such a manner that their allocation positions in individual codes do not overlap with each other.

[0028] Thus, it offers an advantage of being able to prevent the error because of the effect of fading.

[0029] When adding each dummy bit to the data in the multi-level modulation and demodulation method in accordance with the present invention, one of dummy bits of "0" and "1" may be selected and disposed such that a signal constellation of the symbol is placed at an innermost possible region.

[0030] Thus, it offers an advantage of being able to reduce the transmission power of the symbol.

[0031] In the multi-level modulation and demodulation method in accordance with the present invention, one of the dummy bits of "0" and "1" may be selected with reference to a table that defines a bit value of each dummy bit that will place the signal constellation of the symbol at the innermost possible region.

[0032] Thus, it offers an advantage of being able to reduce the transmission power of the symbol easily without complicating the configuration.

BRIEF DESCRIPTION OF THE DRAWINGS

[0033]

Fig. 1 is a diagram showing a bit stream of data and a signal constellation of symbols;

Fig. 2 is a diagram showing an insertion state of dummy bits;

Fig. 3 is a block diagram showing a configuration of a multi-level modulation and demodulation system to which a multi-level modulation and demodulation method of an embodiment 1 in accordance with the present invention is applied;

Fig. 4 is a flowchart illustrating a multi-level modulation method of the embodiment 1 in accordance with the present invention;

Fig. 5 is a flowchart illustrating a multi-level demodulation method of the embodiment 1 in accordance with the present invention;

Fig. 6 is a diagram showing a bit stream of data and a signal constellation of symbols;

Fig. 7 is a diagram showing a signal constellation of symbols when dummy bits are inserted;

Fig. 8 is a diagram showing a bit stream of data and a signal constellation of symbols;

Fig. 9 is a diagram showing a signal constellation of symbols when dummy bits are inserted;

Fig. 10 is a diagram showing distributed allocation of dummy bits;

Fig. 11 is a diagram showing an allocation example of dummy bits;

Fig. 12 is a diagram showing an allocation example of dummy bits when transmitting data in multi-code;

Fig. 13 is a table predefining bit values of dummy bits for allocating a signal constellation of symbols at the innermost possible region;

Fig. 14 is the table predefining the bit values of the dummy bits for allocating the signal constellation of symbols at the innermost possible region;

Fig. 15 is a diagram showing a 16-QAM signal constellation;

Fig. 16 is a diagram illustrating power of individual 16-QAM phase points;

Fig. 17 is a table predefining bit values of dummy bits for allocating a signal constellation of symbols at the innermost possible region;

Fig. 18 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 19 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 20 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 21 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 22 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 23 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 24 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 25 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 26 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 27 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 28 is the table predefining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 29 is the table predelining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 30 is the table predelining the bit values of the dummy bits for allocating the signal constellation of the symbols at the innermost possible region;

Fig. 31 is a diagram showing a 64-QAM signal constellation; and

Fig. 32 is a diagram illustrating power of individual 64-QAM phase points.

BEST MODE FOR CARRYING OUT THE INVENTION

[0034] The best mode for carrying out the present invention will now be described with reference to the accompanying drawings.

EMBODIMENT 1

[0035] Fig. 3 is a block diagram showing a configuration of a multi-level modulation and demodulation system to which a multi-level modulation and demodulation method of an embodiment 1 in accordance with the present invention is applied. In this figure, the reference numeral 1 designates a multi-level modulator for carrying out multi-level modulation of data consisting of a plurality of symbols to be transmitted, and 2 designates a multi-level demodulator for receiving the data consisting of a plurality of symbols and carrying out multi-level demodulation.

[0036] The reference numeral 11 designates a transmission data generator for generating the data consisting of a plurality of symbols; 12 designates a channel encoder for mapping the bit stream of the data in such a manner that a specified bit of at least one symbol becomes a dummy bit, when the number of bits of the data generated by the data transmission data generator 11 is less than that of the radio frame; 13 designates a modulation stage for carrying out multi-level modulation of the data output from the channel encoder 12; 14 designates an RF stage for causing an antenna 15 to make radio transmission of the data passing through the multi-level modulation by the modulation stage 13; and 15 designates the antenna.

[0037] The reference numeral 16 designates an antenna; 17 designates an RF stage for receiving the data transmitted from the multi-level modulator 1; 18 designates a demodulation stage for carrying out multi-level demodulation of the data received by the RF stage 17; and 19 designates a channel decoder for removing the dummy bits from the data passing through the multi-level demodulation by the demodulation stage 18.

[0038] Fig. 4 is a flowchart illustrating the multi-level modulation method of the embodiment 1 in accordance with the present invention; and Fig. 5 is a flowchart illustrating the multi-level demodulation method of the embodiment 1 in accordance with the present invention.

[0039] Next, the operation will be described.

[0040] First, the transmission data generator 11 of the multi-level modulator 1 generates the data consisting of a plurality of symbols as illustrated in Fig. 6 (step ST1). For convenience of explanation, it is assumed that each symbol generates 4-bit data, and undergoes 16-QAM multi-level modulation.

[0041] When the number of bits of the data generated by the transmission data generator 11 is less than that of the radio frame, the channel encoder 12 cannot perform the multi-level modulation of the data. Accordingly, it maps the bit stream of the data such that a specified bit of at least one symbol becomes a dummy bit (steps ST2 and ST3).

[0042] Specifically, as illustrated in Fig. 7, the channel encoder 12 maps the bit stream of the data such that the lowest two bits of the symbols are assigned dummy bits of "0". The signal points of the symbols with their lowest two bits being "0" are allocated at shaded regions adjacent to the origin of an IQ coordinate system, at which the power amplitude is zero. Therefore, the transmission power of the symbols is small.

[0043] When the channel encoder 12 completes the mapping of the bit stream of the data, the modulation stage 13 carries out the multi-level modulation of the data after mapping (step ST4).

[0044] The RF stage 14 conducts the radio transmission of the data passing through the multi-level modulation by the modulation stage 13 using the antenna 15 (step ST5).

[0045] On the other hand, the RF stage 17 of the multi-level demodulator 2 receives the data transmitted by the multi-level modulator 1 (step ST11).

[0046] The demodulation stage 18 carries out the multi-level demodulation of the data received by the RF stage 17 (step ST12).

[0047] The channel decoder 19 removes the dummy bits from the data passing through the multi-level demodulation by the demodulation stage 18 (step ST13). Specifically, the channel decoder 19, receiving information about the symbols and bit positions of the dummy bits from the multi-level modulator 1, recognizes the insertion position of the dummy bits, and eliminates the dummy bits from the data. As described above, the embodiment 1 maps the bit stream of the data in such a manner that a specified bit or bits of at least one symbol become dummy bits. As a result, it offers an advantage of being able to reduce the transmission power of the symbol, thereby suppressing the interference to other signals.

[0048] Although the lowest two bits of the symbols are assigned the dummy bits of "0" in the present embodiment 1, this is not essential. For example, the transmission power can be turned off at the positions.

EMBODIMENT 2

[0049] Although the foregoing embodiment 1 carries out the 4-bit 16-QAM of each symbol, the present em-

embodiment 2 conducts 8-bit 64 QAM of each symbol as shown in Fig. 8, in which case, two bits at the middle of the symbol are assigned the dummy bits of "0".

[0050] In this case, as shown in Fig. 9, the signal points of the symbols with their middle two bits being "0" are allocated at shaded regions close to the origin of the IQ coordinate system.

[0051] Thus, the transmission power of the symbols becomes small. To further reduce the transmission power, dummy bits of "1" are assigned to the lowest two bits of the symbol in addition to the dummy bits of "0" assigned to the two bits at the middle of the symbol.

[0052] In this case, as shown in Fig. 9, the signal points of the symbols are assigned to the boldly hatched region adjacent to the origin of the IQ coordinate system.

[0053] Incidentally, when the symbols undergo the 6-bit 64 QAM, the technique of turning off the transmission power is also applicable.

EMBODIMENT 3

[0054] Although the foregoing embodiments 1 and 2 allocate the dummy bits to the predetermined bits of the symbol, this is not essential. In the present embodiment 3, the dummy bits are distributed among a plurality of symbols as shown in Fig. 10, thereby mapping the dummy bits as uniform as possible.

[0055] Thus, the present embodiment 3 offers an advantage of being able to prevent errors because of the effect of fading.

EMBODIMENT 4

[0056] Although the dummy bits are distributed among a plurality of symbols in the embodiment 3, Fig. 11 shows another distribution, in which each symbol is assigned two dummy bits. If some dummy bits remain unassigned after completing the assignment of the dummy bits to all the symbols, they are assigned to some symbols four bits per symbol (in the example Fig. 11, the 4-bit allocation of the dummy bits is made to the right-hand symbols).

[0057] Thus, the present embodiment 4 offers an advantage of being able to prevent the error because of the effect of fading, and to reduce the transmission power of the data as a whole.

EMBODIMENT 5

[0058] Although the embodiments 1-4 do not mention, when the multi-level modulator 1 transmits data in multi-code as shown in Fig. 12, the dummy bits of the individual codes are mapped such that their positions do not overlap with each other.

[0059] Thus, the present embodiment 5 offers an advantage of being able to prevent the error because of the effect of fading.

EMBODIMENT 6

[0060] Although the foregoing embodiment 1 handles the case that places the dummy bits of "0" at the lowest two bits of the symbols, the dummy bits can be disposed in other positions as follows.

[0061] More specifically, as for the 16-QAM where each symbol consists of four bits {b0, b1, b2, b3}, 24 = 81 ways of bit combinations will occur at the same probability when the occurrence probabilities of "0", "1" and "DTX (dummy bits)" are equal.

[0062] To reduce the transmission power of the symbol that includes at least one dummy bit in the four bits {b0, b1, b2, b3}, the dummy bit is replaced by "0" or "1" so that the signal constellation of the symbol is placed at the inmost possible regions.

[0063] More specifically, when a symbol includes at least one dummy bit in the four bits {b0, b1, b2, b3}, the dummy bit is replaced by "0" or "1" with reference to the table as shown in Figs. 13 and 14, which predetermines the bit values of the dummy bits in order to place the signal constellation of the symbols at the inmost possible regions. For example, when {b0, b1, b2, b3} = {D, D, 1, 0}, it is replaced by {0, 0, 1, 0}.

[0064] Fig. 15 is a diagram showing a 16-QAM signal constellation, and Fig. 16 is a diagram showing the power of the phase points.

[0065] As shown in Fig. 16, the power at the phase points A in Fig. 15 is least, the power at the phase points B is second, and the power at the phase points C is greatest. Therefore, the table is formed such that the phase points of the bit arrangement after the replacement become the phase points A whenever possible. The table is formed such that when the phase points A cannot be assigned, the phase points B are assigned, and when the phase points B cannot be assigned, the phase points C are assigned.

EMBODIMENT 7

[0066] Although the foregoing embodiment 6 is applied to the 16-QAM where each symbol consists of four bits {b0, b1, b2, b3}, this is not essential. For example, the technique is also applicable to the 64-QAM where each symbol consists of six bits {b0, b1, b2, b3, b4, b5}.

[0067] As for the 64-QAM, 36 = 729 ways of bit combinations will occur at the same probability when the occurrence probabilities of "0", "1" and "DTX (dummy bits)" are equal.

[0068] When a symbol includes at least one dummy bit in the six bits {b0, b1, b2, b3, b4, b5}, the dummy bit is replaced by "0" or "1" with reference to the table as shown in Figs. 17-20, which predetermines the bit values of the dummy bits in order to place the signal constellation of the symbols at the inmost possible regions. For example, when {b0, b1, b2, b3, b4, b5} = {0, 0, D, 1, D, 0}, it is replaced by {0, 0, 0, 1, 1, 0}.

[0069] Fig. 21 is a diagram showing a 64-QAM signal

constellation, and Fig. 32 is a diagram showing the power of the phase points.

[0070] As shown in Fig. 32, the power increases from the phase points A to I. Therefore, the table is formed such that the phase points of the bit arrangement after the replacement become the phase points A or B whenever possible.

INDUSTRIAL APPLICABILITY

[0071] As described above, the multi-level modulation and demodulation method in accordance with the present invention is suitable for the system required to reduce the transmission power of the data, and the interference to other signals.

[0072] More specifically, they are suitable for the multi-level modulation system at the transmitting side of a base station or a mobile station.

Claims

1. A multi-level modulation method of performing, when transmitting data consisting of a plurality of symbols after multi-level modulation of the data, the multi-level modulation of the data by adding a dummy bit to the data when a number of bits of the data is less than a number of bits of a radio frame, the multi-level modulation method comprising the step of mapping a bit stream of the data such that a specified bit of at least one symbol becomes a dummy bit.
2. A multi-level demodulation method of performing multi-level demodulation of data consisting of a plurality of symbols by receiving the data, the multi-level demodulation method comprising the step of performing the multi-level demodulation of the data considering that a specified bit of at least one symbol is a dummy bit.
3. A multi-level modulation and demodulation method comprising the steps of: mapping, when a number of bits of data consisting of a plurality of symbols is less than a number of bits of a radio frame, a bit stream of the data such that a specified bit of at least one symbol becomes a dummy bit; transmitting the data after mapping by passing the data through multi-level modulation; and performing, when receiving the data, the multi-level demodulation of the data considering that at least one specified bit of the symbol is a dummy bit.
4. The multi-level modulation and demodulation method according to claim 3, wherein when the symbol undergoes 4-bit 16 QAM, a dummy bit of "0" is assigned to the lowest two bits of the symbol.
5. The multi-level modulation and demodulation method according to claim 3, wherein when the symbol undergoes 4-bit 16 QAM, transmission power is turned off.
6. The multi-level modulation and demodulation method according to claim 3, wherein when the symbol undergoes 6-bit 64 QAM, a dummy bit of "0" is assigned to intermediate two bits of the symbol.
7. The multi-level modulation and demodulation method according to claim 3, wherein when the symbol undergoes 6-bit 64 QAM, a dummy bit of "0" is assigned to intermediate two bits of the symbol and a dummy bit of "1" is assigned to lowest two bits of the symbol.
8. The multi-level modulation and demodulation method according to claim 3, wherein when the symbol undergoes 8-bit 64 QAM, transmission power is turned off.
9. The multi-level modulation and demodulation method according to claim 3, wherein when the dummy bits are added to the data, the dummy bits are distributed to a plurality of symbols.
10. The multi-level modulation and demodulation method according to claim 3, wherein two dummy bits are assigned to each of the symbols, and when the dummy bits are left even after assigning the two dummy bits to each of all the symbols, another two bits are assigned to some of the symbols.
11. The multi-level modulation and demodulation method according to claim 3, wherein when carrying out multi-code transmission of the data, the dummy bits are mapped such that their allocation positions in individual codes do not overlap with each other.
12. The multi-level modulation and demodulation method according to claim 3, wherein when adding each dummy bit to the data, one of dummy bits of "0" and "1" is selected and disposed such that a signal constellation of the symbol is placed at an inmost possible region.
13. The multi-level modulation and demodulation method according to claim 12, wherein one of the dummy bits of "0" and "1" is selected with reference to a table that defines a bit value of each dummy bit that will place the signal constellation of the symbol at the inmost possible region.

FIG. 1

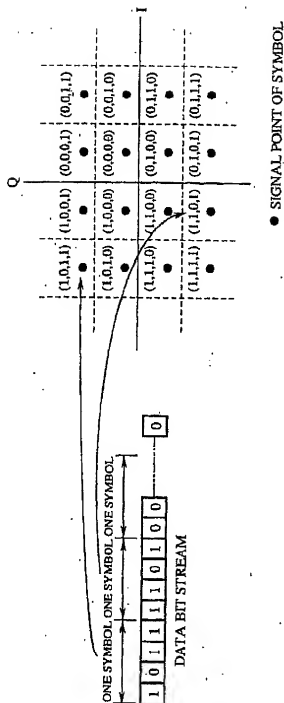


FIG.2

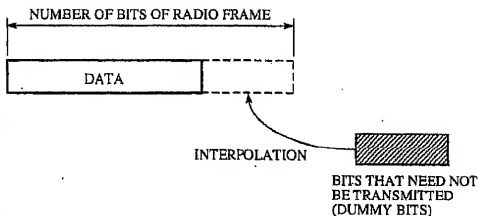


FIG.5

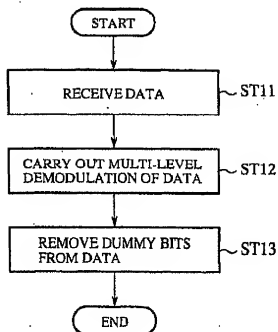


FIG.3

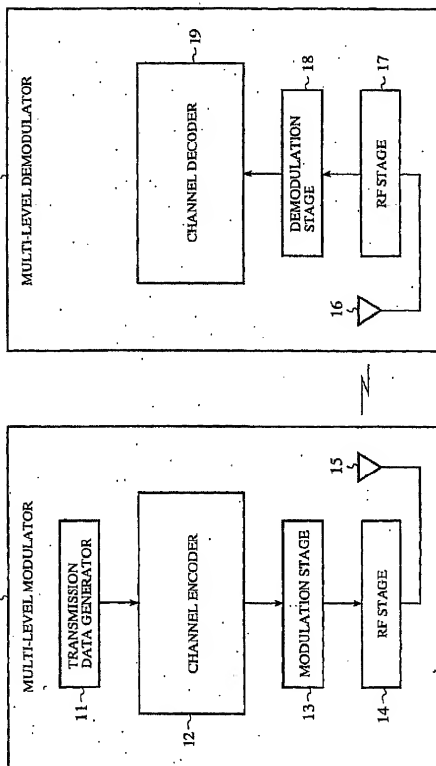


FIG.4

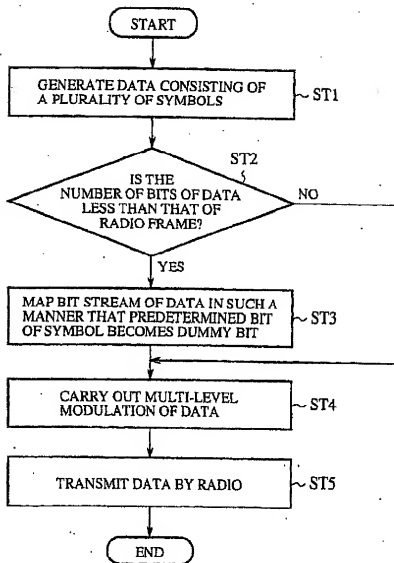


FIG. 6

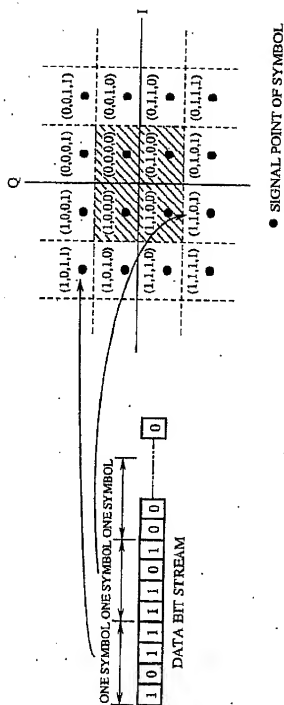


FIG. 7

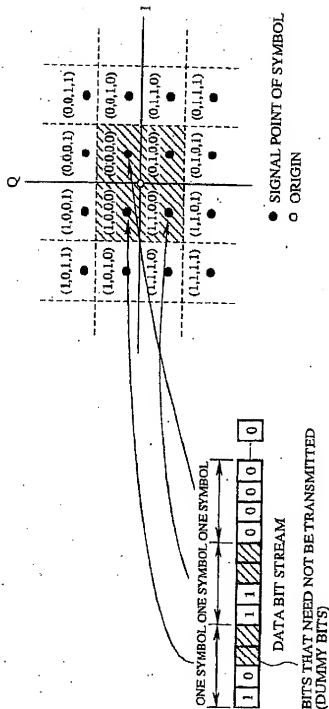


FIG. 8

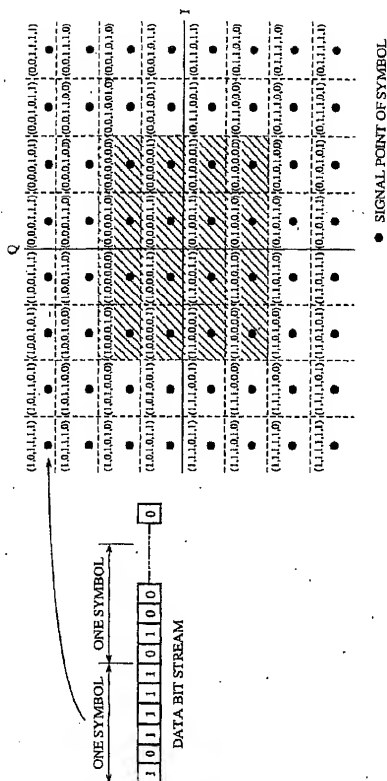


FIG.9

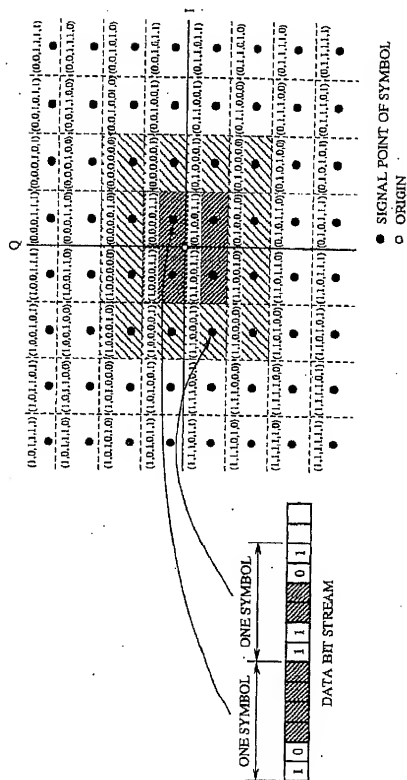


FIG.10

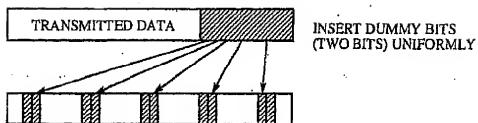


FIG.11

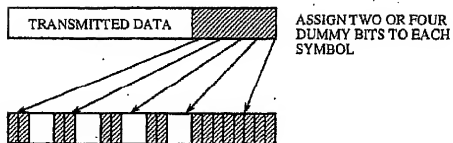
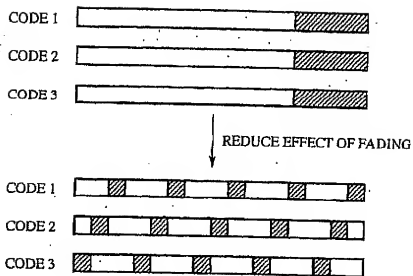


FIG.12



BEFORE REPLACEMENT				AFTER REPLACEMENT			POWER
b0	b1	b2	b3	c0	c1	c2	
0	0	0	0	0	0	0	2
0	0	0	D	0	0	0	2
0	0	0	1	0	0	0	10
0	0	D	0	0	0	0	2
0	0	D	D	0	0	0	2
0	0	D	1	0	0	0	10
0	0	1	0	0	0	1	0
0	0	1	D	0	0	1	0
0	0	1	1	0	0	1	18
0	D	0	0	0	0	0	2
0	D	0	D	0	0	0	2
0	D	0	1	0	0	0	10
0	D	D	0	0	0	0	2
0	D	D	D	0	0	0	2
0	D	D	1	0	0	0	10
0	D	1	0	0	0	1	0
0	D	1	D	0	0	1	0
0	D	1	1	0	0	1	18
0	1	0	0	0	1	0	0
0	1	0	D	0	1	0	0
0	1	0	1	0	1	0	10
0	1	D	0	0	1	0	0
0	1	D	D	0	1	0	2
0	1	D	1	0	1	0	10
0	1	1	0	0	1	1	0
0	1	1	D	0	1	1	0
0	1	1	1	0	1	1	18
D	0	0	0	0	0	0	2
D	0	0	D	0	0	0	2
D	0	0	1	0	0	0	10
D	0	D	0	0	0	0	2
D	0	D	D	0	0	0	2
D	0	D	1	0	0	0	10
D	0	1	0	0	0	1	0
D	0	1	D	0	0	1	0
D	0	1	1	0	0	1	18
D	D	0	0	0	0	0	2
D	D	0	D	0	0	0	2
D	D	0	1	0	0	0	10
D	D	D	0	0	0	0	2
D	D	D	D	0	0	0	2
D	D	D	1	0	0	0	10
D	D	1	0	0	0	1	0
D	D	1	D	0	0	1	0
D	D	1	1	0	0	1	18
D	1	0	0	0	1	0	0
D	1	0	D	0	1	0	0
D	1	0	1	0	1	0	10
D	1	D	0	0	1	0	2
D	1	D	D	0	1	0	2
D	1	D	1	0	1	0	10
D	1	1	0	0	1	1	0
D	1	1	D	0	1	1	0
D	1	1	1	0	1	1	18
1	0	0	0	1	0	0	2

FIG.13

FIG.14.

1	0	0	D	1	0	0	0	2
1	0	0	1	1	0	0	1	10
1	0	D	0	1	0	0	0	2
1	0	D	D	1	0	0	0	2
1	0	D	1	1	0	0	1	10
1	0	1	0	1	0	1	0	10
1	0	1	D	1	0	1	0	10
1	0	1	1	1	0	1	1	18
1	D	0	0	1	0	0	0	2
1	D	0	D	1	0	0	0	2
1	D	0	1	1	0	0	1	10
1	D	D	0	1	0	0	0	2
1	D	D	D	1	0	0	0	2
1	D	D	1	1	0	0	1	10
1	D	1	0	1	0	1	0	10
1	D	1	D	1	0	1	0	10
1	D	1	1	1	0	1	1	18
1	1	0	0	1	1	0	0	2
1	1	0	D	1	1	0	0	2
1	1	0	1	1	1	0	1	10
1	1	D	0	1	1	0	0	2
1	1	D	D	1	1	0	0	2
1	1	D	1	1	1	0	1	10
1	1	1	0	1	1	1	0	10
1	1	1	D	1	1	1	0	10
1	1	1	1	1	1	1	1	18

FIG.15

1011 C	1001 B	0001 B	0011 C
1010 B	1000 A	0000 A	0010 B
1110 B	1100 A	0100 A	0110 B
1111 C	1101 B	0101 B	0111 C

FIG.16

$18\alpha^2$	$10\alpha^2$	$10\alpha^2$	$18\alpha^2$
$10\alpha^2$	$2\alpha^2$	$2\alpha^2$	$10\alpha^2$
$10\alpha^2$	$2\alpha^2$	$2\alpha^2$	$10\alpha^2$
$18\alpha^2$	$10\alpha^2$	$10\alpha^2$	$18\alpha^2$

[illegible]

FIG.18

0	D	D	0	D	0	0	0	0	0	1	0
0	D	D	0	D	D	0	0	0	0	1	1
0	D	D	0	D	1	0	0	0	0	1	1
0	D	D	0	1	0	0	0	0	0	1	0
0	D	D	0	1	D	0	0	0	0	1	1
0	D	D	0	1	1	0	0	0	0	1	1
0	D	D	D	0	0	0	0	0	0	0	0
0	D	D	D	0	D	0	0	0	0	0	1
0	D	D	D	0	1	0	0	0	0	0	1
0	D	D	D	0	0	0	0	0	0	1	0
0	D	D	D	D	0	0	0	0	0	1	1
0	D	D	D	D	1	0	0	0	0	1	1
0	D	D	D	1	0	0	0	0	0	1	0
0	D	D	D	1	D	0	0	0	0	1	1
0	D	D	D	1	1	0	0	0	0	1	1
0	D	D	D	1	1	0	0	0	0	1	0
0	D	D	D	1	1	0	0	0	0	1	1
0	D	D	1	0	0	0	0	0	1	0	0
0	D	D	1	0	D	0	0	0	1	0	0
0	D	D	1	0	1	0	0	0	1	0	1
0	D	D	1	D	0	0	0	0	1	1	0
0	D	D	1	D	D	0	0	0	1	1	0
0	D	D	1	D	1	0	0	0	1	1	1
0	D	D	1	D	1	0	0	0	1	1	1
0	D	D	1	1	0	0	0	0	1	1	0
0	D	D	1	1	D	0	0	0	1	1	0
0	D	D	1	1	1	0	0	0	1	1	1
0	D	1	0	0	0	0	0	1	0	0	0
0	D	1	0	0	D	0	0	1	0	0	1
0	D	1	0	0	1	0	0	1	0	0	1
0	D	1	0	D	0	0	0	1	0	0	0
0	D	1	0	D	D	0	0	1	0	0	1
0	D	1	0	D	1	0	0	1	0	0	1
0	D	1	0	1	0	0	0	1	0	1	0
0	D	1	0	1	D	0	0	1	0	1	1
0	D	1	0	1	1	0	0	1	0	1	1
0	D	1	D	0	0	0	0	1	0	0	0
0	D	1	D	0	D	0	0	1	0	0	1
0	D	1	D	0	1	0	0	1	0	0	1
0	D	1	D	D	0	0	0	1	0	0	0
0	D	1	D	D	D	0	0	1	0	0	1
0	D	1	D	1	0	0	0	1	0	0	1
0	D	1	D	1	D	0	0	1	0	1	1
0	D	1	D	1	1	0	0	1	0	1	1
0	D	1	1	0	0	0	0	1	1	0	0
0	D	1	1	0	D	0	0	1	1	0	0
0	D	1	1	0	1	0	0	1	1	0	1
0	D	1	1	D	0	0	0	1	1	0	0
0	D	1	1	D	D	0	0	1	1	0	0
0	D	1	1	D	1	0	0	1	1	0	1
0	D	1	1	1	0	0	0	1	1	1	1
0	1	0	0	0	0	0	1	0	0	0	0
0	1	0	0	0	D	0	1	0	0	0	1
0	1	0	0	0	1	0	1	0	0	0	1
0	1	0	0	D	0	0	1	0	0	1	0
0	1	0	0	D	D	0	1	0	0	1	1

FIG.19

FIG.21

0	1	1	0	1	D	0	1	1	0	1	1
0	1	1	0	1	1	0	1	1	0	1	1
0	1	1	D	0	0	0	1	1	0	0	0
0	1	1	D	0	D	0	1	1	0	0	0
0	1	1	D	0	1	0	1	1	D	0	1
0	1	1	D	D	0	0	1	1	0	0	0
0	1	1	D	D	D	0	1	1	0	0	0
0	1	1	D	D	1	0	1	1	0	1	1
0	1	1	D	1	0	0	1	1	0	1	0
0	1	1	D	1	D	0	1	1	0	1	1
0	1	1	D	1	1	0	1	1	0	1	1
0	1	1	1	0	0	0	1	1	1	0	0
0	1	1	1	0	D	0	1	1	1	0	0
0	1	1	1	0	1	0	1	1	1	0	1
0	1	1	1	D	0	0	1	1	1	0	0
0	1	1	1	D	D	0	1	1	1	0	0
0	1	1	1	D	1	0	1	1	1	0	1
0	1	1	1	1	0	0	1	1	1	1	0
0	1	1	1	1	D	0	1	1	1	1	0
0	1	1	1	1	1	0	1	1	1	1	1
D	0	0	0	0	0	0	0	0	0	0	0
D	0	0	0	0	D	0	0	0	0	0	1
D	0	0	0	0	1	0	0	0	0	0	1
D	0	0	0	D	0	0	0	0	0	1	0
D	0	0	0	D	D	0	0	0	0	1	1
D	0	0	0	D	1	0	0	0	0	1	1
D	0	0	0	1	0	0	0	0	0	1	0
D	0	0	0	1	D	0	0	0	0	1	1
D	0	0	0	1	1	0	0	0	0	1	1
D	0	0	D	0	0	0	0	0	0	0	0
D	0	0	D	0	D	0	0	0	0	0	1
D	0	0	D	0	1	0	0	0	0	0	1
D	0	0	D	D	0	0	0	0	0	1	0
D	0	0	D	D	D	0	0	0	0	1	1
D	0	0	D	D	1	0	0	0	0	1	1
D	0	0	D	1	0	0	0	0	0	1	0
D	0	0	D	1	D	0	0	0	0	1	1
D	0	0	D	1	1	0	0	0	0	1	1
D	0	0	1	0	0	0	0	0	1	0	0
D	0	0	1	0	D	0	0	0	1	0	0
D	0	0	1	0	1	0	0	0	1	0	1
D	0	0	1	D	0	0	0	0	1	1	0
D	0	0	1	D	D	0	0	0	1	1	0
D	0	0	1	D	1	0	0	0	1	1	1
D	0	0	1	1	0	0	0	0	1	1	0
D	0	0	1	1	D	0	0	0	1	1	0
D	0	0	1	1	1	0	0	0	1	1	1
D	0	D	0	0	0	0	0	0	0	0	0
D	0	D	0	0	D	0	0	0	0	0	1
D	0	D	0	0	1	0	0	0	0	0	1
D	0	D	0	D	0	0	0	0	0	1	0
D	0	D	0	D	D	0	0	0	0	1	1
D	0	D	0	D	1	0	0	0	0	1	1
D	0	D	0	1	0	0	0	0	0	1	0
D	0	D	0	1	D	0	0	0	0	1	1
D	0	D	0	1	1	0	0	0	0	1	1

D	0	D	0	0	0	0	0	0	0	0	0
D	0	D	D	0	D	0	0	0	0	0	1
D	0	D	D	0	1	0	0	0	0	0	1
D	0	D	D	D	0	0	0	0	0	1	0
D	0	D	D	D	D	0	0	0	0	1	1
D	0	D	D	D	1	0	0	0	0	1	1
D	0	D	D	1	0	0	0	0	0	1	0
D	0	D	D	1	D	0	0	0	0	1	1
D	0	D	D	1	1	0	0	0	0	1	1
D	0	D	1	0	0	0	0	0	1	0	0
D	0	D	1	0	D	0	0	0	1	0	0
D	0	D	1	0	1	0	0	0	1	0	1
D	0	D	1	D	0	0	0	0	1	1	0
D	0	D	1	D	D	0	0	0	1	1	0
D	0	D	1	D	1	0	0	0	1	1	1
D	0	D	1	1	0	0	0	0	1	1	0
D	0	D	1	1	D	0	0	0	1	1	0
D	0	D	1	1	1	0	0	0	1	1	1
D	0	1	0	0	0	0	0	1	0	0	0
D	0	1	0	0	D	0	0	1	0	0	1
D	0	1	0	0	1	0	0	1	0	0	1
D	0	1	0	D	0	0	0	1	0	0	0
D	0	1	0	D	D	0	0	1	0	0	1
D	0	1	0	D	1	0	0	1	0	0	1
D	0	1	0	1	0	0	0	1	0	1	0
D	0	1	0	1	D	0	0	1	0	1	1
D	0	1	0	1	1	0	0	1	0	1	1
D	0	1	D	0	0	0	0	1	0	0	0
D	0	1	D	0	D	0	0	1	0	0	1
D	0	1	D	0	1	0	0	1	0	0	1
D	0	1	D	D	0	0	0	1	0	0	0
D	0	1	D	D	D	0	0	1	0	0	1
D	0	1	D	D	1	0	0	1	0	0	1
D	0	1	D	1	0	0	0	1	0	1	0
D	0	1	D	1	D	0	0	1	0	1	1
D	0	1	D	1	1	0	0	1	0	1	1
D	0	1	1	0	0	0	0	1	1	0	0
D	0	1	1	0	D	0	0	1	1	0	0
D	0	1	1	0	1	0	0	1	1	0	1
D	0	1	1	D	0	0	0	1	1	0	0
D	0	1	1	D	D	0	0	1	1	0	0
D	0	1	1	D	1	0	0	1	1	0	1
D	0	1	1	1	0	0	0	1	1	1	0
D	0	1	1	1	D	0	0	1	1	1	0
D	0	1	1	1	1	0	0	1	1	1	1
D	D	0	0	0	0	0	0	0	0	0	0
D	D	0	0	0	D	0	0	0	0	0	1
D	D	0	0	0	1	0	0	0	0	0	1
D	D	0	0	D	0	0	0	0	0	1	0
D	D	0	0	D	D	0	0	0	0	1	1
D	D	0	0	D	1	0	0	0	0	1	1
D	D	0	0	1	0	0	0	0	0	1	0
D	D	0	0	1	D	0	0	0	0	1	1
D	D	0	0	1	1	0	0	0	0	1	1
D	D	0	D	0	0	0	0	0	0	0	0
D	D	0	D	0	D	0	0	0	0	0	1

FIG.22

FIG.23

D	D	0	D	0	1	0	0	0	0	0	1
D	D	0	D	D	0	0	0	0	0	1	0
D	D	0	D	D	D	0	0	0	0	1	1
D	D	0	D	D	1	0	0	0	0	1	1
D	D	0	D	1	0	0	0	0	0	1	0
D	D	0	D	1	D	0	0	0	0	1	1
D	D	0	D	1	1	0	0	0	0	1	1
D	D	0	1	0	0	0	0	0	1	0	0
D	D	0	1	0	1	0	0	0	1	0	0
D	D	0	1	0	1	0	0	0	1	0	1
D	D	0	1	D	0	0	0	0	1	1	0
D	D	0	1	D	D	0	0	0	1	1	0
D	D	0	1	D	1	0	0	0	1	1	1
D	D	0	1	1	0	0	0	0	1	1	0
D	D	0	1	1	D	0	0	0	1	1	0
D	D	0	1	1	1	0	0	0	1	1	1
D	D	D	0	0	0	0	0	0	0	0	0
D	D	D	0	0	D	0	0	0	0	0	1
D	D	D	0	0	1	0	0	0	0	0	1
D	D	D	0	D	0	0	0	0	0	1	0
D	D	D	0	D	D	0	0	0	0	1	1
D	D	D	0	D	1	0	0	0	0	1	1
D	D	D	0	1	0	0	0	0	0	1	0
D	D	D	0	1	D	0	0	0	0	1	1
D	D	D	0	1	1	0	0	0	0	1	1
D	D	D	D	0	0	0	0	0	0	0	0
D	D	D	D	0	D	0	0	0	0	0	1
D	D	D	D	0	1	0	0	0	0	0	1
D	D	D	D	D	0	0	0	0	0	1	0
D	D	D	D	D	D	0	0	0	0	1	1
D	D	D	D	D	1	0	0	0	0	1	1
D	D	D	D	1	0	0	0	0	0	1	0
D	D	D	D	1	D	0	0	0	0	1	1
D	D	D	D	1	1	0	0	0	0	1	1
D	D	D	1	0	0	0	0	0	1	0	0
D	D	D	1	0	D	0	0	0	1	0	0
D	D	D	1	0	1	0	0	0	1	0	1
D	D	D	1	D	0	0	0	0	1	1	0
D	D	D	1	D	D	0	0	0	1	1	0
D	D	D	1	D	1	0	0	0	1	1	1
D	D	D	1	1	0	0	0	0	1	1	0
D	D	D	1	1	D	0	0	0	1	1	0
D	D	D	1	1	1	0	0	0	1	1	1
D	D	1	0	0	0	0	0	1	0	0	0
D	D	1	0	0	D	0	0	1	0	0	1
D	D	1	0	0	1	0	0	1	0	0	1
D	D	1	0	D	0	0	0	1	0	0	0
D	D	1	0	D	D	0	0	1	0	0	1
D	D	1	0	D	1	0	0	1	0	0	1
D	D	1	0	1	0	0	0	1	0	1	0
D	D	1	0	1	D	0	0	1	0	1	1
D	D	1	0	1	1	0	0	1	0	1	1
D	D	1	D	0	0	0	0	1	0	0	0
D	D	1	D	0	D	0	0	1	0	0	1
D	D	1	D	0	1	0	0	1	0	0	1
D	D	1	D	D	0	0	0	1	0	0	0

D	D	1	D	h	D	0	0	1	0	0	1
D	D	1	D	D	1	0	0	1	0	0	1
D	D	1	D	1	0	0	0	1	0	1	0
D	D	1	D	1	D	0	0	1	0	1	1
D	D	1	D	1	1	0	0	1	0	1	1
D	D	1	1	0	0	0	0	1	1	0	0
D	D	1	1	0	D	0	0	1	1	0	0
D	D	1	1	0	1	0	0	1	1	0	1
D	D	1	1	D	0	0	0	1	1	0	0
D	D	1	1	D	D	0	0	1	1	0	0
D	D	1	1	D	1	0	0	1	1	0	1
D	D	1	1	1	0	0	0	1	1	1	0
D	D	1	1	1	D	0	0	1	1	1	0
D	D	1	1	1	1	0	0	1	1	1	1
D	1	0	0	0	0	0	1	0	0	0	0
D	1	0	0	0	D	0	1	0	0	0	1
D	1	0	0	0	1	0	1	0	0	0	1
D	1	0	0	D	0	0	1	0	0	1	0
D	1	0	0	D	D	0	1	0	0	1	1
D	1	0	0	D	1	0	1	0	0	1	1
D	1	0	0	1	0	0	1	0	0	1	0
D	1	0	0	1	D	0	1	0	0	1	1
D	1	0	0	1	1	0	1	0	0	1	1
D	1	0	D	0	0	0	1	0	0	0	0
D	1	0	D	0	D	0	1	0	0	0	1
D	1	0	D	0	1	0	1	0	0	0	1
D	1	0	D	D	0	0	1	0	0	1	0
D	1	0	D	D	D	0	1	0	0	1	1
D	1	0	D	D	1	0	1	0	0	1	1
D	1	0	D	1	0	0	1	0	0	1	0
D	1	0	D	1	D	0	1	0	0	1	1
D	1	0	D	1	1	0	1	0	0	1	1
D	1	0	1	0	0	0	1	0	1	0	0
D	1	0	1	0	D	0	1	0	1	0	0
D	1	0	1	0	1	0	1	0	1	0	1
D	1	0	1	D	0	0	1	0	1	1	0
D	1	0	1	D	1	0	1	0	1	1	1
D	1	0	1	1	0	0	1	0	1	1	0
D	1	0	1	1	D	0	1	0	1	1	0
D	1	0	1	1	1	0	1	0	1	1	1
D	1	D	0	0	0	0	1	0	0	0	0
D	1	D	0	0	D	0	1	0	0	0	1
D	1	D	0	0	1	0	1	0	0	0	1
D	1	D	0	D	0	0	1	0	0	1	1
D	1	D	0	D	D	0	1	0	0	1	1
D	1	D	0	D	1	0	1	0	0	1	1
D	1	D	0	1	0	0	1	0	0	1	0
D	1	D	0	1	D	0	1	0	0	1	1
D	1	D	0	1	1	0	1	0	0	1	1
D	1	D	D	0	D	0	1	0	0	0	1
D	1	D	D	0	1	0	1	0	0	0	1
D	1	D	D	D	0	0	1	0	0	1	0
D	1	D	D	D	D	0	1	0	0	1	1
D	1	D	D	D	1	0	1	0	0	1	1

FIG.24

FIG. 25

D	1	D	D	1	0	0	1	0	0	1	0
D	1	D	D	1	D	0	1	0	0	1	1
D	1	D	D	1	1	0	1	0	0	1	1
D	1	D	1	0	0	0	1	0	1	0	0
D	1	D	1	0	D	0	1	0	1	0	0
D	1	D	1	0	1	0	1	0	1	0	1
D	1	D	1	D	0	0	1	0	1	1	0
D	1	D	1	D	D	0	1	0	1	1	0
D	1	D	1	D	1	0	1	0	1	1	1
D	1	D	1	1	0	0	1	0	1	1	0
D	1	D	1	1	D	0	1	0	1	1	0
D	1	D	1	1	1	0	1	0	1	1	1
D	1	1	0	0	0	0	1	1	0	0	0
D	1	1	0	0	D	0	1	1	0	0	0
D	1	1	0	0	1	0	1	1	0	0	1
D	1	1	0	D	0	0	1	1	0	0	0
D	1	1	0	D	D	0	1	1	0	0	0
D	1	1	0	D	1	0	1	1	0	1	1
D	1	1	0	1	0	0	1	1	0	1	0
D	1	1	0	1	D	0	1	1	0	1	1
D	1	1	0	1	1	0	1	1	0	1	1
D	1	1	D	0	0	0	1	1	0	0	0
D	1	1	D	0	D	0	1	1	0	0	0
D	1	1	D	0	1	0	1	1	0	0	1
D	1	1	D	D	0	0	1	1	0	0	0
D	1	1	D	D	D	0	1	1	0	0	0
D	1	1	D	D	1	0	1	1	0	1	1
D	1	1	D	1	0	0	1	1	0	1	0
D	1	1	D	1	D	0	1	1	0	1	1
D	1	1	D	1	1	0	1	1	0	1	1
D	1	1	1	0	0	0	1	1	1	0	0
D	1	1	1	0	D	0	1	1	1	0	0
D	1	1	1	0	1	0	1	1	1	0	1
D	1	1	1	D	0	0	1	1	1	0	0
D	1	1	1	D	D	0	1	1	1	0	0
D	1	1	1	D	1	0	1	1	1	0	1
D	1	1	1	1	0	0	1	1	1	1	0
D	1	1	1	1	D	0	1	1	1	1	0
D	1	1	1	1	1	0	1	1	1	1	1
1	0	0	0	0	0	1	0	0	0	0	0
1	0	0	0	0	D	1	0	0	0	0	1
1	0	0	0	0	1	1	0	0	0	0	1
1	0	0	0	D	0	1	0	0	0	1	0
1	0	0	0	D	D	1	0	0	0	1	1
1	0	0	0	D	1	1	0	0	0	1	1
1	0	0	0	1	0	1	0	0	0	1	0
1	0	0	0	1	D	1	0	0	0	1	1
1	0	0	0	1	1	1	0	0	0	1	1
1	0	0	D	0	0	1	0	0	0	0	0
1	0	0	D	0	D	1	0	0	0	0	1
1	0	0	D	0	1	1	0	0	0	0	1
1	0	0	D	D	0	1	0	0	0	1	0
1	0	0	D	D	D	1	0	0	0	1	1
1	0	0	D	D	1	1	0	0	0	1	1
1	0	0	D	1	0	1	0	0	0	1	0
1	0	0	D	1	D	1	0	0	0	1	1

1	0	0	D	1	1	1	0	0	0	1	1
1	0	0	1	0	0	1	0	0	1	0	0
1	0	0	1	0	D	1	0	0	1	0	0
1	0	0	1	0	1	1	0	0	1	0	1
1	0	0	1	D	0	1	0	0	1	1	0
1	0	0	1	D	D	1	0	0	1	1	0
1	0	0	1	D	1	1	0	0	1	1	1
1	0	0	1	1	0	1	0	0	1	1	0
1	0	0	1	1	D	1	0	0	1	1	0
1	0	0	1	1	1	1	0	0	1	1	1
1	D	D	0	0	0	1	0	0	0	0	0
1	0	D	0	0	D	1	0	0	0	0	1
1	0	D	0	0	0	1	1	0	0	0	1
1	0	D	0	D	0	1	0	0	0	1	0
1	0	D	0	D	D	1	0	0	0	1	1
1	0	D	0	D	1	1	0	0	0	1	1
1	0	D	0	1	0	1	0	0	0	1	0
1	0	D	0	1	D	1	0	0	0	1	1
1	0	D	0	1	1	1	0	0	0	1	1
1	0	D	D	0	0	1	0	0	0	0	0
1	0	D	D	0	D	1	0	0	0	0	1
1	0	D	D	0	1	1	0	0	0	0	1
1	0	D	D	D	0	1	0	0	0	1	0
1	0	D	D	D	D	1	0	0	0	1	1
1	0	D	D	D	1	1	0	0	0	1	1
1	0	D	D	1	0	1	0	0	0	1	0
1	0	D	D	1	D	1	0	0	0	1	1
1	0	D	D	1	1	1	0	0	0	1	1
1	0	D	1	0	0	1	0	0	1	0	0
1	0	D	1	0	D	1	0	0	1	0	0
1	0	D	1	0	1	0	0	1	0	1	1
1	0	D	1	D	D	1	0	0	1	1	0
1	0	D	1	D	1	1	0	0	1	1	1
1	0	D	1	1	0	1	0	0	1	1	0
1	0	D	1	1	D	1	0	0	1	1	0
1	0	D	1	1	1	1	0	0	1	1	1
1	0	1	0	0	0	1	0	1	0	0	0
1	0	1	0	0	D	1	0	1	0	0	0
1	0	1	0	0	1	1	0	1	0	0	1
1	0	1	0	D	0	1	0	1	0	0	0
1	0	1	0	D	D	1	0	1	0	0	1
1	0	1	0	D	1	1	0	1	0	0	1
1	0	1	0	1	0	1	0	1	0	1	0
1	0	1	0	1	0	1	0	1	0	1	1
1	0	1	0	1	1	1	0	1	0	1	1
1	0	1	D	0	0	1	0	1	0	0	0
1	0	1	D	0	D	1	0	1	0	0	1
1	0	1	D	0	1	1	0	1	0	0	1
1	0	1	D	D	0	1	0	1	0	0	0
1	0	1	D	D	D	1	0	1	0	0	1
1	0	1	D	D	1	1	0	1	0	0	1
1	0	1	D	1	0	1	0	1	0	1	0
1	0	1	D	1	D	1	0	1	0	1	1
1	0	1	D	1	1	1	0	1	0	1	1
1	0	1	1	0	0	1	0	1	1	0	0

FIG.26

FIG.27

1	0	1	1	0	D	1	0	1	1	0	0
1	0	1	1	0	1	1	0	1	1	0	1
1	0	1	1	D	0	1	0	1	1	0	0
1	0	1	1	D	D	1	0	1	1	0	0
1	0	1	1	D	1	1	0	1	1	0	1
1	0	1	1	1	0	1	0	1	1	1	0
1	0	1	1	1	D	1	0	1	1	1	0
1	0	1	1	1	1	1	0	1	1	1	1
1	D	0	0	0	0	1	0	0	0	0	0
1	D	0	0	0	D	1	0	0	0	0	1
1	D	0	0	0	1	1	0	0	0	0	1
1	D	0	0	D	0	1	0	0	0	1	0
1	D	0	0	D	D	1	0	0	0	1	1
1	D	0	0	D	1	1	0	0	0	1	1
1	D	0	0	1	0	1	0	0	0	1	0
1	D	0	0	1	D	1	0	0	0	1	1
1	D	0	0	1	1	1	0	0	0	1	1
1	D	0	D	0	0	1	0	0	0	0	0
1	D	0	D	0	D	1	0	0	0	0	1
1	D	0	D	0	1	1	0	0	0	0	1
1	D	0	D	D	0	1	0	0	0	1	0
1	D	0	D	D	D	1	0	0	0	1	1
1	D	0	D	D	1	1	0	0	0	1	1
1	D	0	D	1	0	1	0	0	0	1	0
1	D	0	D	1	D	1	0	0	0	1	1
1	D	0	D	1	1	1	0	0	0	1	1
1	D	0	1	0	0	1	0	0	1	0	0
1	D	0	1	0	D	1	0	0	1	0	0
1	D	0	1	0	1	1	0	0	1	0	1
1	D	0	1	D	0	1	0	0	1	1	0
1	D	0	1	D	D	1	0	0	1	1	0
1	D	0	1	D	1	1	0	0	1	1	1
1	D	0	1	1	1	1	0	0	1	1	1
1	D	D	0	0	0	1	0	0	0	0	0
1	D	D	0	0	D	1	0	0	0	0	1
1	D	D	0	0	1	1	0	0	0	0	1
1	D	D	0	D	0	1	0	0	0	1	0
1	D	D	0	D	0	1	0	0	0	1	0
1	D	D	0	D	D	1	0	0	0	1	1
1	D	D	0	D	1	1	0	0	0	1	1
1	D	D	0	1	0	1	0	0	0	1	0
1	D	D	0	1	D	1	0	0	0	1	1
1	D	D	0	1	1	1	0	0	0	1	1
1	D	D	0	1	1	1	0	0	0	1	1
1	D	D	0	0	1	0	0	0	0	0	0
1	D	D	D	0	D	1	0	0	0	0	1
1	D	D	D	0	1	1	0	0	0	0	1
1	D	D	D	D	0	1	0	0	0	1	0
1	D	D	D	D	D	1	0	0	0	1	1
1	D	D	D	D	1	1	0	0	0	1	1
1	D	D	D	1	0	1	0	0	0	1	0
1	D	D	D	1	D	1	0	0	0	1	1
1	D	D	D	1	1	1	0	0	0	1	1
1	D	D	1	0	0	1	0	0	1	0	0
1	D	D	1	0	D	1	0	0	1	0	0
1	D	D	1	0	1	1	0	0	1	0	1

1	D	D	1	D	0	1	0	0	1	1	0
1	D	D	1	D	D	1	0	0	1	1	0
1	D	D	1	D	1	1	0	0	1	1	1
1	D	D	1	1	0	1	0	0	1	1	0
1	D	D	1	1	D	1	0	0	1	1	0
1	D	D	1	1	1	1	0	0	1	1	1
1	D	1	0	0	0	1	0	1	0	0	0
1	D	1	0	0	D	1	0	1	0	0	1
1	D	1	0	0	1	1	0	1	0	0	1
1	D	1	0	D	0	1	0	1	0	0	0
1	D	1	0	D	D	1	0	1	0	0	1
1	D	1	0	D	1	1	0	1	0	0	1
1	D	1	0	1	0	1	0	1	0	1	0
1	D	1	0	1	D	1	0	1	0	1	1
1	D	1	0	1	1	1	0	1	0	1	1
1	D	1	D	0	0	1	0	1	0	0	0
1	D	1	D	0	D	1	0	1	0	0	1
1	D	1	D	0	1	1	0	1	0	0	1
1	D	1	D	D	0	1	0	1	0	0	0
1	D	1	D	D	D	1	0	1	0	0	1
1	D	1	D	1	0	1	0	1	0	0	1
1	D	1	D	1	D	1	0	1	0	1	1
1	D	1	D	1	1	1	0	1	0	1	1
1	D	1	1	0	0	1	0	1	1	0	0
1	D	1	1	0	D	1	0	1	1	0	0
1	D	1	1	0	1	1	0	1	1	0	1
1	D	1	1	D	0	1	0	1	1	0	0
1	D	1	1	D	D	1	0	1	1	0	0
1	D	1	1	D	1	1	0	1	1	0	1
1	D	1	1	1	0	1	0	1	1	1	0
1	D	1	1	1	1	1	0	1	1	1	1
1	1	0	0	0	0	1	1	0	0	0	0
1	1	0	0	0	D	1	1	0	0	0	1
1	1	0	0	0	1	1	1	0	0	0	1
1	1	0	0	D	0	1	1	0	0	1	0
1	1	0	0	D	1	1	1	0	0	1	1
1	1	0	0	1	0	1	1	0	0	1	0
1	1	0	0	1	D	1	1	0	0	1	1
1	1	0	0	1	1	1	1	0	0	1	1
1	1	0	D	0	0	1	1	0	0	0	0
1	1	0	D	0	D	1	1	0	0	0	1
1	1	0	D	0	1	1	1	0	0	0	1
1	1	0	D	D	0	1	1	0	0	1	0
1	1	0	D	D	D	1	1	0	0	1	1
1	1	0	D	D	1	1	1	0	0	1	0
1	1	0	D	1	D	1	1	0	0	1	1
1	1	0	D	1	1	1	1	0	0	1	1
1	1	0	1	0	0	1	1	0	1	0	0
1	1	0	1	0	D	1	1	0	1	0	1
1	1	0	1	D	0	1	1	0	1	1	0
1	1	0	1	D	D	1	1	0	1	1	0

FIG.28

FIG. 29

1	1	0	1	D	1	1	1	0	1	1	1
1	1	0	1	1	0	1	1	0	1	1	0
1	1	0	1	1	D	1	1	0	1	1	0
1	1	0	1	1	1	1	1	0	1	1	1
1	1	D	0	0	0	1	1	0	0	0	0
1	1	D	0	0	D	1	1	0	0	0	1
1	1	D	0	0	1	1	1	0	0	0	1
1	1	D	0	D	0	1	1	0	0	1	0
1	1	D	0	D	D	1	1	0	0	1	1
1	1	D	0	D	1	1	1	0	0	1	1
1	1	D	0	1	0	1	1	0	0	1	0
1	1	D	0	1	D	1	1	0	0	1	1
1	1	D	0	1	1	1	1	0	0	1	1
1	1	D	D	0	0	1	1	0	0	0	0
1	1	D	D	0	D	1	1	0	0	0	0
1	1	D	D	0	1	1	1	0	0	0	1
1	1	D	D	D	0	1	1	0	0	1	0
1	1	D	D	D	D	1	1	0	0	1	1
1	1	D	D	D	1	1	1	0	0	1	1
1	1	D	D	1	0	1	1	0	0	1	0
1	1	D	D	1	D	1	1	0	0	1	1
1	1	D	D	1	1	1	1	0	0	1	1
1	1	D	1	0	0	1	1	0	1	0	0
1	1	D	1	0	D	1	1	0	1	0	0
1	1	D	1	0	1	1	1	0	1	0	1
1	1	D	1	D	0	1	1	0	1	1	0
1	1	D	1	D	D	1	1	0	1	1	0
1	1	D	1	D	1	1	1	0	1	1	1
1	1	D	1	1	0	1	1	0	1	1	0
1	1	D	1	1	D	1	1	0	1	1	0
1	1	D	1	1	1	1	1	0	1	1	1
1	1	1	0	0	0	1	1	1	0	0	0
1	1	1	0	0	D	1	1	1	0	0	1
1	1	1	0	0	1	1	1	1	0	0	1
1	1	1	0	D	0	1	1	1	0	0	0
1	1	1	0	D	D	1	1	1	0	0	1
1	1	1	0	D	1	1	1	1	0	0	1
1	1	1	0	1	0	1	1	1	0	1	0
1	1	1	0	1	D	1	1	1	0	1	1
1	1	1	0	1	1	1	1	1	0	1	1
1	1	1	D	0	0	1	1	1	0	0	0
1	1	1	D	0	D	1	1	1	0	0	1
1	1	1	D	0	1	1	1	1	0	0	1
1	1	1	D	D	0	1	1	1	0	0	0
1	1	1	D	D	D	1	1	1	0	0	1
1	1	1	D	D	1	1	1	1	0	0	1
1	1	1	D	1	0	1	1	1	0	1	1
1	1	1	D	1	1	1	1	1	0	1	1
1	1	1	D	1	1	1	1	1	0	1	1
1	1	1	1	0	0	1	1	1	1	0	0
1	1	1	1	0	D	1	1	1	1	0	0
1	1	1	1	0	1	1	1	1	1	0	1
1	1	1	1	D	0	1	1	1	1	0	0
1	1	1	1	D	D	1	1	1	1	0	0
1	1	1	1	D	1	1	1	1	1	0	1
1	1	1	1	1	0	1	1	1	1	1	0

FIG.30

1	1	1	1	1	D	1	1	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1

FIG.31

101111 I	101101 H	100101 G	100111 F	000111 F	000101 G	001101 H	001111 I
101110 H	101100 F	100100 E	100110 D	000110 D	000100 E	001100 F	001110 H
101010 G	101000 E	100000 C	100010 B	000010 B	000000 C	001000 E	001010 G
101011 F	101001 D	100001 B	100011 A	000011 A	000001 B	001001 D	001011 F
111011 F	111001 D	110001 B	110011 A	010011 A	010001 B	011001 D	011011 F
111010 G	111000 E	110000 C	110010 B	010010 B	010000 C	011000 E	011010 G
111110 H	111100 F	110100 E	110110 D	010110 D	010100 E	011100 F	011110 H
111111 I	111101 H	110101 G	110111 F	010111 F	010101 G	011101 H	011111 I

FIG.32

$98 \alpha^2$	$74 \alpha^2$	$58 \alpha^2$	$50 \alpha^2$	$50 \alpha^2$	$58 \alpha^2$	$74 \alpha^2$	$98 \alpha^2$
$74 \alpha^2$	$50 \alpha^2$	$34 \alpha^2$	$26 \alpha^2$	$26 \alpha^2$	$34 \alpha^2$	$50 \alpha^2$	$74 \alpha^2$
$58 \alpha^2$	$34 \alpha^2$	$18 \alpha^2$	$10 \alpha^2$	$10 \alpha^2$	$18 \alpha^2$	$34 \alpha^2$	$58 \alpha^2$
$50 \alpha^2$	$26 \alpha^2$	$10 \alpha^2$	$2 \alpha^2$	$2 \alpha^2$	$10 \alpha^2$	$26 \alpha^2$	$50 \alpha^2$
$50 \alpha^2$	$26 \alpha^2$	$10 \alpha^2$	$2 \alpha^2$	$2 \alpha^2$	$10 \alpha^2$	$26 \alpha^2$	$50 \alpha^2$
$58 \alpha^2$	$34 \alpha^2$	$18 \alpha^2$	$10 \alpha^2$	$10 \alpha^2$	$18 \alpha^2$	$34 \alpha^2$	$58 \alpha^2$
$74 \alpha^2$	$50 \alpha^2$	$34 \alpha^2$	$26 \alpha^2$	$26 \alpha^2$	$34 \alpha^2$	$50 \alpha^2$	$74 \alpha^2$
$98 \alpha^2$	$74 \alpha^2$	$58 \alpha^2$	$50 \alpha^2$	$50 \alpha^2$	$58 \alpha^2$	$74 \alpha^2$	$98 \alpha^2$

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP01/04182

A. CLASSIFICATION OF SUBJECT MATTER
Int. Cl.⁷ H04L 27/38

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Int. Cl.⁷ H04L 27/00-27/38

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Jitsuyo Shinan Koho 1926-2001 Toroku Jitsuyo Shinan Koho 1994-2001

Kokai Jitsuyo Shinan Koho 1971-2001

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, 11-177527, A (Matsushita Electric Ind. Co., Ltd.), 02 July, 1999 (02.07.99), Figs. 5, 9 (Family: none)	1-3, 9, 10
Y	JP, 63-156450, A (Fujitsu Limited), 29 June, 1988 (29.06.88), Fig. 5; page 1, lower right column, line 17 to page 2, left column, line 3 (Family: none)	1-3, 9, 10
Y	JP, 02-279033, A (MBC Corporation), 15 November, 1990 (15.11.90), Fig. 3; page 2, lower right column, lines 8-9 (Family: none)	1-3, 9, 10
Y	JP, 10-13404, A (YAZAKI CORPORATION), 16 January, 1998 (16.01.98), Fig. 1; page 3, right column, lines 9-13 (Family: none)	9
A	JP, 10-136046, A (Advanced Digital Television Broadcasting), 22 May, 1998 (22.05.98),	1-13

☒ Further documents are listed in the continuation of Box C.☐ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not

considered to be of particular relevance

"E" earlier document but published on or after the international filing

date

"I" document which may throw doubts on priority claim(s) or which is

cited to establish the publication date of another citation or other

special reasons (as specified)

"O" documents relating to an oral disclosure, use, exhibition or other

means

"P" document published prior to the international filing date but later

than the priority date claimed

"T" later document published after the international filing date or

priority date and not in conflict with the application but cited to

understand the principle or theory underlying the invention

document of particular relevance; the claimed invention cannot be

equivalent thereof or cannot be considered to involve an inventive

step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be

equivalent thereof or cannot be considered to involve an inventive

step when the document is combined with one or more other cited documents, such

combination being obvious to a person skilled in the art

"A" document member of the same patent family

Date of the actual completion of the international search

06 June, 2001 (06.06.01)

Date of mailing of the international search report

19 June, 2001 (19.06.01)

Name and mailing address of the ISA/
Japanese Patent Office

Authorized officer

Facsimile No.

Telephone No.

Form PCT/ISA/210 (second sheet) (July 1992)

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP01/04182

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
	Figs. 1, 10 (Family: none)	
A	JP, 08-501665, A (Ericsson/ GR Mobile Communications), 20 February, 1996 (20.02.96), Fig. 1; page 17, lines 15 to 20, & US, 5239557, A	1-13
A	JP, 61-281654, A (Kenwood Corporation), 12 December, 1986 (12.12.86), Fig. 2 (Family: none)	1-13
A	JP, 62-176243, A (NEC Corporation), 03 August, 1987 (03.08.87), Figs. 1, 3; page 2, lower left column, lines 8 to 11 (Family: none)	1-13
A	JP, 11-168522, A (Kokusai Electric Co., Ltd.), 22 June, 1999 (22.06.99), Fig. 4; Par. No. 18 (Family: none)	1-13
A	JP, 09-252326, A (Kokusai Electric Co., Ltd.), 22 September, 1997 (22.09.97), Fig. 4 (Family: none)	1-13
A	JP, 2000-124818, A (Toshiba Corporation), 28 April, 2000 (28.04.00), Fig. 1; Par. No. 48 (Family: none)	5, 8

Form PCT/ISA/210 (continuation of second sheet) (July 1992)